



Job description: Hardware Security Engineer

Intrinsic-ID is hiring:

Role: Hardware Security Engineer

Expected starting date: November 2011

Location: Eindhoven, the Netherlands

Education: Master's degree with 3+ years experience or PhD in Computer Science or Electrical Engineering with specific knowledge in:

- Hardware implementations, VHDL coding, FPGAs
- Cryptographic algorithms, error correction algorithms
- Side-channel and fault attacks and countermeasures
- Back-ground in Physical Unclonable Functions (PUF) is a plus

Task description:

1. Research; this role includes studying cryptographic primitives and error correction algorithms used for PUF-based secure key storage in light of side-channel and fault attacks, implementing such primitives on FPGAs.
2. Product development; contribute to the development of new hardware designs for Intrinsic-ID products. Implement and test side-channel and fault attack countermeasures on FPGAs and ASICs. Write design specifications, test benches, simulation models and implement hardware IP in VHDL RTL.

Personal skills:

Highly motivated team player, excellent oral and written English required, independent with strong technical background.

Please contact: careers@intrinsic-id.com, www.intrinsic-id.com.