

DEMO: RESCURE: Retrofit Security for Critical Infrastructures

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ABSTRACT

Low-cost interconnected devices, so-called Internet-of-Things (IoT), commonly have no dedicated or possess insufficient hardware security features. This is challenging, as IoT devices are becoming an integral part of critical infrastructures providing much needed additional functionality but also creating a significant security threat to the infrastructure. Due to the scale of IoT integration in critical infrastructures, a key issue in initial deployment and replacing of the devices is often the cost. RESCURE delivers a low-cost IoT security solution based on unique hardware anchors. More precisely, we are using PUFs (Physical Unclonable Function) technology based on SRAM (Static Random-Access Memory), which provides a unique and unclonable identifier as well as a root key for each device. As SRAM-PUFs-based approaches require no additional specialized hardware, it also presents a viable approach of retrofitting existing embedded devices already used.

KEYWORDS

Internet-of-Things, Embedded systems security, Physically Unclonable Function, End-to-end encryption, multiple observations

1 INTRODUCTION

The IoT device, as defined by ARM [1], is a piece of hardware mostly equipped with a sensor transmitting data over the internet. As cost saving measure, IoT devices are often based on small, inexpensive, and resource constrained chips. This design choice helps with scalability, as IoT devices usually have wide deployment, but reduces support for existing security solutions which often rely on dedicated hardware security features. Furthermore, IoT devices are often using M2M (Machine-to-Machine) communication, have 24/7 uptime and are deployed in field, making them harder to access physically and replace. Due to given risk factors, many recent attacks target on IoT devices as the first step in order to compromise the underlying infrastructures [7]. RESCURE is a European research project, focused on developing low-cost IoT security solution for device protection and secure communication while keeping selected approach applicable to existing devices. The US Department of Homeland Security [6] recommends that devices rely on hardware with incorporated security features, e.g. Arm TrustZone [2]. In RESCURE, to cover a wide range of devices while following set recommendations, we focus on the most commonly available hardware component of IoT devices, namely the SRAM. Generating the root key using SRAM-PUF technology is a low-cost alternative to storing a key in protected memory. Furthermore,

since the SRAM is already available on any IoT device, our scheme supports retrofitting the existing hardware to a secure system.

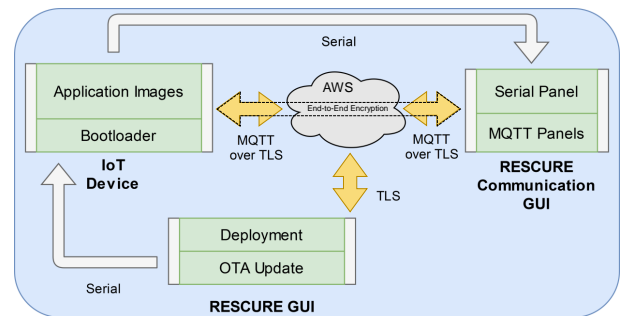


Figure 1: OTA update & deployment flow of demonstrator

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Due to inherent process variation during the manufacturing of SRAM, small and uncontrolled variations occur in the silicon material giving each SRAM a unique initial state [4]. The initial state is not constant but varies between each SRAM power up phase to a certain degree, no matter the production line. Nevertheless, as the intra-subject difference is much less pronounced than the inter-subject difference, even between SRAMs from the same manufacturer, this enables us to uniquely identify devices based on the initial state and generate appropriate root key [3]. In order to turn the noisy SRAM initial state into a reliable and device-unique root key, a helper data scheme is applied on top of SRAM-PUF. For error correction, we implemented an algorithm based on concatenation of BCH code (15,7,5) and repetition code (7,1). Given the 5% inter-subject difference observed on the device at room temperature, our helper data scheme can regenerate the root key with the error rate of 10^{-7} .

In RESCURE, we implemented three distinct security features based on the root key extracted from the SRAM-PUF: 1) secure connection and device identification with the cloud; 2) E2E (End-to-end) encrypted communication with backend; and 3) secure OTA (Over-the-air) software/firmware update.

The first usage of SRAM-PUF root key, in RESCURE, is a runtime generation of SECP256R1 key pair. During the device enrolment phase, we capture the key pair calculated on the device and generate an appropriate device certificate, which we register with the cloud provider (in our case Amazon Web Services). This enables us to tie the key pair, and therefore SRAM-PUF root key, with a thing ID, a unique identifier with whom Amazon identifies the device.

3.2 D2: Multiple Observations Helper Data Scheme

In this demonstrator, we aim to illustrate performance and security of the multiple observations helper data scheme. The demo is running in MATLAB, where we use a statistical model[5] to simulate the SRAM-PUF observations. Furthermore, we use a concatenated LDPC(256,128) and repetition code as the error-correcting code. During the demo we visualize the helper data construction and key reconstruction in real-time. We vary the number of used observations, and plot the resulting reconstruction error rate (FER) in real time. We show that FER decreases when more observations are used. Furthermore, it is possible to vary the rate of the error-correcting code, by adjusting the used repetition rate. Note that a smaller rate means increased efficiency of the scheme, since less SRAM cells are required to achieve the same key length. The simulation results show that a similar FER can be achieved with smaller repetition rate and thus considering multiple observations can improve efficiency of the scheme.

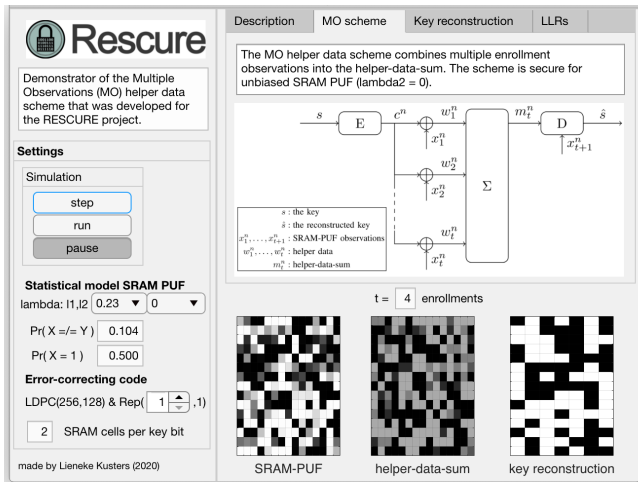


Figure 3: RESCURE MATLAB GUI

Finally, we calculate the log-likelihood ratios (LLRs) after observing the new helper data, both for the decoder and for an attacker. For the decoder, it shows how the updated helper data (based on more observations) improves the reliability of the estimated code bits. For the attacker, it represents the information leakage about the code bits. The result shows that for an unbiased SRAM-PUF, the LLRs of the attacker are constant and do not change when more observations are used. Therefore, it should convince a viewer that no leakage occurs as a result of the multiple observations helper data for unbiased SRAM-PUFs. Instead, for a biased SRAM-PUF the derived LLRs show that information about the code bits is leaked to an attacker. Therefore, the scheme is not secure in case of biased SRAM-PUFs.

A screenshot of the MATLAB GUI is shown in Figure 3.

4 CONCLUSION

We presented SRAM-PUF based security enhancements developed in RESCURE. The objective of this project is to provide a suitable

and cost-effective way to retrofit existing devices by adding tamper-protection, secure storage, end-to-end communication encryption, unclonable ID and device authentication. We aim to increase the security of IoT critical infrastructures providing solution applicable to a variety of devices including low-end, resource-constrained devices.

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